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Abstract
The applications of RFID have been already became the present trend, and frequency bands adopted at present are mostly near UHF and 2.45GHz. The applications of RFID include the electronic toll collection (ETC) system, the identification, the smart card, the application of administing products, and so on. In the thesis, we would like to design the RFID at 5.8 GHz, and apply the circuit design to medical scientific research. The major work of this thesis is to design the front-end of a 5.8GHz RFID receiver. We use the soft tool, ADS, to simulate and design these circuits with the TSMC-SiGe-0.35 process. We will introduce LNA, MIXER and VCO of the receiver circuit in order. The parts of LNA and MIXER in the receiver have been implemented on a chip, and the VCO is only simulated by ADS.
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